## S/N 09/256,643

**PATENT** 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Serial No.:

09/256,643

Filed:

February 23, 1999

Title:

TRANSISTOR WITH VARIABI

METHODS OF FABRICATION AND THE

Examiner: Michael Trinh

Group Art Unit: 2822

Docket: 303.324US2

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for review in connection with the above-identified patent application. Pursuant to the provisions of MPEP 609. Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner be returned to the Applicants.

In accordance with 37 C.F.R. §1.98(d), some of the copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicants' prior U.S. application, Serial No. 08/903,452, filed on July 29, 1997, which is relied upon for an earlier filing date under 35 U.S.C. §120.

Applicants have included the fee of \$240.00 under 37 C.F.R §§ 1.97(c) and 1.17(p). Please charge any additional fees or credit any overpayment to Account No. 19-0743.

Applicants respectfully request consideration of these references during prosecution of the above-identified matter. The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES ET AL.

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By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 554

(612) 373-6973

5 OCT 2000 Date

Robert E. Mates Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 👤 day of October, 2000

Name

Signature